

(19) World Intellectual Property  
Organization  
International Bureau



(43) International Publication Date  
23 June 2005 (23.06.2005)

PCT

(10) International Publication Number  
**WO 2005/057655 A1**

(51) International Patent Classification<sup>7</sup>: **H01L 23/60**  
// H05F 3/02

(21) International Application Number:  
PCT/FI2004/050176

(22) International Filing Date:  
24 November 2004 (24.11.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
20035238 15 December 2003 (15.12.2003) FI

(71) Applicant (for all designated States except US): **NOKIA CORPORATION** [FI/FI]; Keilalahdentie 4, FI-02150 Espoo (FI).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **PEKKARI-NEN, Ari** [FI/FI]; Riihikuja 2, FI-41340 Laukaa (FI). **SAUKONOJA, Pasi** [FI/FI]; Huutoniementie 111, FI-44190 Parantala (FI).

(74) Agent: **BERGGREN OY AB**; Ohjelmakaari 1, FI-40500 Jyväskylä (FI).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

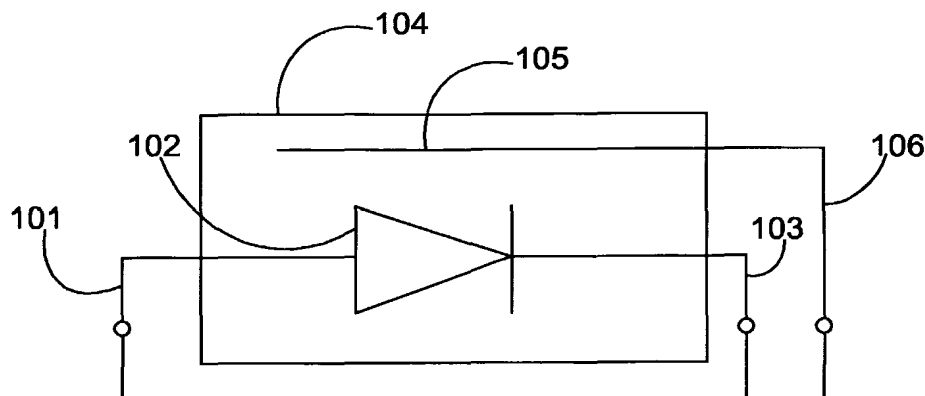
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND ARRANGEMENT FOR SHIELDING A COMPONENT AGAINST ELECTROSTATIC INTERFERENCE



(57) Abstract: The invention relates to an apparatus and arrangement for shielding a component, particularly a semiconductor component, against electrostatic discharge. The semiconductor component (102, 202, 302) according to an embodiment of the invention comprises an electroconductive element (105, 205, 305), for which there is arranged at least one outlet (106, 206, 306) so that the electroconductive element (105, 205, 305) is groundable through said outlet (106, 206, 306) for shielding the semiconductor component (102, 202, 302) against electrostatic pulses.

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